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endian processes of a biendian machine). Another is the **compile**-time problem of specifying a data layout portably, so that a single version of an Ada ...

www.ada-auth.org/ai-files/grab_bag/bitorder.pdf - [Similar pages](#)Linux-Kernel Archive: **compile** a big-**endian** kernel on a prep machine

i want to **compile** the kernel in big-**endian** ordering. i saw : processor.h --> MSR_LE, MSR_ILE byteorder.h mkprep.h can you give me advices about it ? ...

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Is any **compile** flag applied to convert the little **endian** to big **endian** to save my life for the porting? I am using Sun Studio 11 with Sun C 5.8 Patch ...

forum.sun.com/jive/thread.jspa?threadID=99144&messageID=340061 - 28k -[Cached](#) - [Similar pages](#)Using the GNU **Compiler** Collection (GCC)

Note: this option should only be used if you require compatibility with code for big-**endian** ARM processors generated by versions of the **compiler** prior to ...

gcc.gnu.org/onlinedocs/gcc-3.2.3/gcc/ARM-Options.html - 16k -[Cached](#) - [Similar pages](#)Re: How do I build universal binaries for libraries with **endian** ...

Re: How do I build universal binaries for libraries with **endian**-specific code ... have **endian**-specific code that is included or removed at **compile** time. ...

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[Ffmpeg-devel] ffmpeg cross-**compile** for big-**endian** armv5. John Clonts john at clonts.org Fri May 26 05:47:13 CEST 2006. Previous message: [Ffmpeg-devel] ...

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port-sgimips: kernel **compile**: big **endian** not recognized properly

Subject: kernel **compile**: big **endian** not recognized properly ... locore.o: compiled for a big **endian** system and target is little **endian** File in wrong format: ...
mail-index.netbsd.org/port-sgimips/2001/10/26/0006.html - 3k - [Cached](#) - [Similar pages](#)

SDSC User Services : IA-64 Linux Cluster User Guide : **Compiling**

This enables the development and processing of files with big-endian data ... X
compiler) allow the display of source file traceback information at runtime ...
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Roland Caßeböhm - No address munging for platforms don't need to

Little-endian version -#if (CYG_BYTEORDER == CYG_LSBFIRST) +// Little-endian version or big-endian version that doesn't need address munging +#if ...
ecos.sourceforge.org/ml/ecos-patches/2003-11/msg00016.html - 11k -
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BYTE.com

To accommodate smaller scalars, a technique known as address munging is used. When the PowerPC is in little-endian mode, it transforms the 3 low-order bits ...
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[PDF] MPC8xx True Little-Endian Mode

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Big-Endian Mode. SDMA byte lane swapper. BO field of FCR. 1x. PowerPC
Little-Endian Mode. Load-Store address munging (3 bits) ...
www.freescale.com/files/archives/doc/app_note/MPC823LEM.pdf - [Similar pages](#)

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It's more complex than this, because it is pseudo little endian, munging low address bits. The memory seen as an array of bytes is not invariant through ...
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... because it is pseudo little endian, > >munging > >low address bits. ... Well, if the processors switched to little endian with byte invariant addressing ...
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Mark Salter - Re: Big endian ARM HAL WRITE UINT8 problem

As an example: If little or big endian, in both systems the receive buffer is > at ... IMO, the address munging is only necessary on badly designed (broken! ...
sources.redhat.com/ml/ecos-discuss/2002-07/msg00414.html - 6k -
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Terme

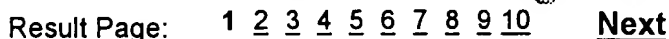
... as little-endian values, when in fact it is stored in big-endian order, ... Note that munging affects only the effective address and not the byte order. ...
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Interbus buffer for use between a pseudo little endian bus and a ...

Address munging is a solution that reorders data transferred from big endian devices to little endian devices. This solution combines two steps. ...
www.freepatentsonline.com/5627975.html - 57k - [Cached](#) - [Similar pages](#)

Re: SH Linux: remove big endian multilib ... disable multilib for (and there's no way to pick a default for these besides munging the spec/fragment files). ...
gcc.gnu.org/ml/gcc-patches/2001-07/msg02059.html - 5k - [Cached](#) - [Similar pages](#)

Most non-Intel-based computers are big **endian**. This presents a number of issues ...
These issues mostly deal with **munging** data between the PCI bus and the ...
[developer.apple.com/.../DeviceDrivers/Conceptual/
WritingPCIDrivers/endianness/chapter_7_section_3.html](http://developer.apple.com/.../DeviceDrivers/Conceptual/WritingPCIDrivers/endianness/chapter_7_section_3.html) - 14k -
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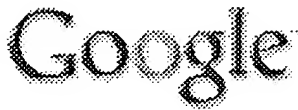


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Andrew Cagney - Re: gdb -> OcdLibRemote -> Raven | Wiggler ...

It put on to the address/data busses: Address: 4 (in binary 100) Data Lane: 0: 1: 2: 3: 4:
A 5: B 6: C 7: D For a little endian store (same address and ...
sources.redhat.com/ml/gdb/2003-09/msg00256.html - 11k - [Cached](#) - [Similar pages](#)

Converting and Porting SPICE Binary Data Files ...

The breakdown by computing environment is as follows: Big Endian IEEE: ... The
binary translation capability provides ``read-only'' access to non-native ...
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address space swapping endian

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You must specify the **address space** and data width of the device you wish to ... These functions include a flag that can be set to "swap" **Endian** styles. ...

[flywheel.princeton.edu/~wfisher/links/VMEIL_useage.html](#) - 6k -

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VMEIL.h File Reference

Closes an existing connection and deallocates **address space** associated with the ... **swap**, Set to 1 for **Endian** byte **swapping**, set to 0 for no **swapping**. ...

[flywheel.princeton.edu/~wfisher/VMEIL/VMEIL_8h.html](#) - 31k - [Cached](#) - [Similar pages](#)

Writing PCI Drivers: Data Structure Order

All writes to PCI configuration **space** are little **endian**. ... In short, the hardware byte **swapping** ensures that the byte order as seen by software is the ...

[developer.apple.com/.../DeviceDrivers/Conceptual/](#)

[WritingPCIDrivers/endianness/chapter_7_section_5.html](#) - 15k -

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Source Code Guidelines

The Solaris Operating System can be used both in big-endian mode (SPARC ... The x86 platform accesses I/O ports through the I/O **address space** by means of a ...

[www.sun.com/software/solaris/programs/source_code_guidelines.xml](#) - 26k -

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SGI TPL (IRIX 6.5: Developer/OrOn2 ProgRef - Chapter 2. M Mode ...

The vector for the cache error handler is located in Ualias **space**. The **address swapping** described above provides the cache error handler of each processor ...

[techpubs.sgi.com/library/tpl/cgi-bin/getdoc.cgi/](#)

[0650/bks/SGI_Developer/books/OrOn2_ProgRef/sgi_html/ch02.html](#) - 37k -

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UNIX man pages : ddi_device_acc_attr (9S)

The devacc_attr_endian_flags member describes the **endian** characteristics of the ...

The size of the **address space** is 128 Kbyte with each 32 Kbyte range as a ...

[bama.ua.edu/cgi-bin/man-cgi?ddi_device_acc_attr+9S](#) - 11k - [Cached](#) - [Similar pages](#)

UNIX man pages : csx_RequestWindow (9F)

The function csx_RequestWindow() requests a block of system **address space** be assigned to a PC Card in a socket. The function csx_ReleaseWindow() releases ...

[bama.ua.edu/cgi-bin/man-cgi?csx_RequestWindow+9F](#) - 14k - [Cached](#) - [Similar pages](#)

VMEbus Architectures

Some VMEbus adapters provide hardware byte **swapping**. Digital's adapters may provide ... The **address** in the VMEbus **address space** where a data transfer occurs ...

[h30097.www3.hp.com/docs/base_doc/](#)

[DOCUMENTATION/HTML/AA-Q0R7F-TE_html/CHPTR002.HTM](#) - 30k -

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Writing VMEbus Device Drivers

Some VMEbus adapters provide hardware byte **swapping**. ... You allocate the VMEbus **address space** for direct memory access operations by calling the following ...

h30097.www3.hp.com/docs/dev_doc/

DOCUMENTATION/HTML/AQ0R7GTE/DOCU_002.HTM - 40k -

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DPANS94

The **address space** of an ANS Forth system is divided into an array of **address** ... For example, the following code for a 16-bit 8086 little **endian** Forth would ...

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[MPC8425 vs Little **Endian** Graphics Adapter vs WindML ...](#) - comp.os.vxworks - Dec 9, 2003

[using little **endian** on sparc v9](#) - comp.unix.solaris - Mar 26, 2001

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Little-endian-to-Big-endian Conversion. The Intel Fortran **Compiler** can write unformatted sequential files in big-endian format and also can read files ...

[www.nersc.gov/vendor_docs/intel/f_ug2/cmp_endi.htm](#) - 13k - [Cached](#) - [Similar pages](#)

Programming on Davinci

The icpc command uses the same **compiler** options as the icc command. ... where type is big for big to little **endian conversion**, and little to specify data in ...

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PowerPC Compiler Writer's Guide - Contents - 11:46am

3.3.2.1 Single-Precision Loads and Stores; 3.3.2.2 Double-Precision Loads and Stores; 3.3.2.3 **Endian Conversion**; 3.3.2.4 Touch Instructions ...

[wall.riscom.net/books/proc/ppc/cwg/cwg_toc.html](#) - 24k - [Cached](#) - [Similar pages](#)

CONVERT specifier - The GNU Fortran 95 Compiler

The GNU Fortran 95 **Compiler**. ... **CONVERT** works between big- and little-endian for INTEGER values of all supported kinds and for REAL on IEEE systems of kinds ...

[gcc.gnu.org/onlinedocs/gcc-4.1.1/gfortran/CONVERT-specifier.html](#) - 5k -

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The GM API - Endian Conversion - 12:16pm

In the absence of automatic checks, **endian conversion** is typically an error-prone ... the **compiler** will report errors if any type **conversion** is missing. ...

[www.millennium.berkeley.edu/docs/mpi/gm_manual/gm_10.html](#) - 3k -

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NCSA IBM pSeries 690: Programming Environment

IBM uses different **compiler** names to perform tasks which are handled by **compiler** ... **Endian Conversion**. The p690 and the Origin2000 machines both use big ...

[www.ncsa.uiuc.edu/UserInfo/Resources/Hardware/IBMp690/Doc/Compile.html](#) - 29k -

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NCSA TeraGrid: Programming Environment

Little endian to big endian **conversion** ... X **compiler**) allow the display of source file traceback information at runtime when a severe error occurs. ...

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ICTP - Scientific Computer Section Portal - Little-endian-to-Big ...

Intel Fortran **Compiler** · IFC User's Guide ... The little-endian-to-big-endian **conversion** feature is intended for Fortran unformatted input/output operations ...

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ACES: Advanced Computing In Environmental Science

Intel **compiler**. Intel C **compiler**; Intel Fortran **compiler**; **Endian conversion** for Fortran application; File name extensions; **Compiler** options. GNU **compiler** ...
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SGI Altix User Guide: Data Conversions - 12:16pm

To use big-endian unformatted files for I/O, file format **conversion** is ... For details of the Intel Fortran **compiler**, refer to the Intel Fortran **Compiler** ...

sc.tamu.edu/help/altix/conversion.shtml - 5k - [Cached](#) - [Similar pages](#)

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